AMENDMENTS TO THE CLAIMS

1. (Original) A computer system comprising:

a plurality of memory caches;

a list containing;

an address reference for every line in the plurality of memory caches for which a corresponding line in memory may not be identical; and an indicator of which cache owns each line; and

the list not containing;

address references for lines that are shared or uncached; and data corresponding to the address references.

2. (Original) The computer system of claim 1 wherein the address reference is a tag.

3. (Canceled)

- 4. (Original) The computer system of claim 1, wherein the number of address references that the list can hold is substantially less than the total number of lines that can be stored in the plurality of memory caches.
- 5. (Original) The computer system of claim 1, wherein the list comprises a single list shared by all devices in the computer system.
- 6. (Original) The computer system of claim 1, wherein every device that can request a copy of memory data maintains a local copy of the list.

7 -10 (Canceled)

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11. (Currently amended) The method of claim 7, further comprising A method for maintaining cache coherency in a computer system, comprising:

entering into a list, an address reference for all lines that are owned; not entering into the list address references for lines that are shared or uncached; updating the list only when ownership of a line changes; and removing an address reference to a line, from the list, when the line has remained in the list for longer than a specified time.

12. (Currently amended) The method of claim 7, further comprising A method for maintaining cache coherency in a computer system, comprising:

entering into a list, an address reference for all lines that are owned;
not entering into the list address references for lines that are shared or uncached;
updating the list only when ownership of a line changes; and
removing an address reference to a line, from the list, even when the list is not full,
to help prevent the list from filling.